Queuing Delay & Packet Loss遗留问题\_2015.11.26 0:51

1. why not input buffer?

XY:有

XM:考察两种情形：

1. output buffer 也作input buffer, that is, 收发都用同一个缓存
2. output buffer and input buffer 独立存在，即收发缓存独立（处理更复杂）

2. how does router determine which packet it should be recive or send ?

XY:这里涉及到路由转发表，总之就是路由器里面配了个表，可以决定哪个IP进来以后从哪里出去。转发表还分动态和静态，静态就是人工配置的，适合于小网络，我们实验室就是静态的。

XM:And, what `s Forward Table ? ——Maybe, I need keep reading?

3. As with postal addresses,this address has a hierarchical structure.

so what`s hierarchical structure means?

XM:如何通过IP地址管理整个网络中的数据传输?

4. each router has a forwarding table that maps destination address(or portions of the destination address)to that router`s outbound links.

so, what`s "map" means?

XY:表。

XM:表的解释不合理，该问题暂时先遗留。

2015-12-13 21:07:58： 问题2、4均是涉及转发表的原理